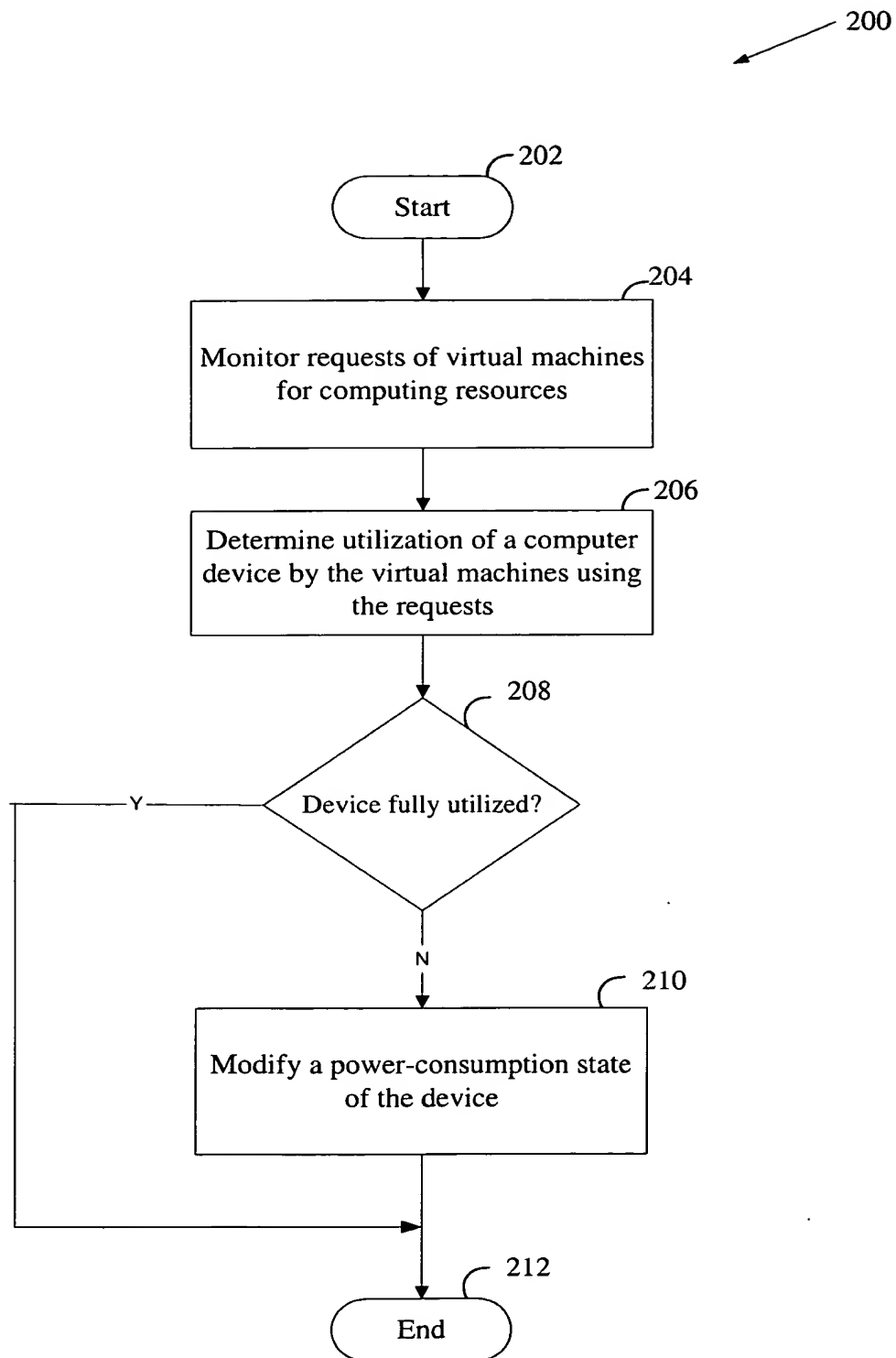
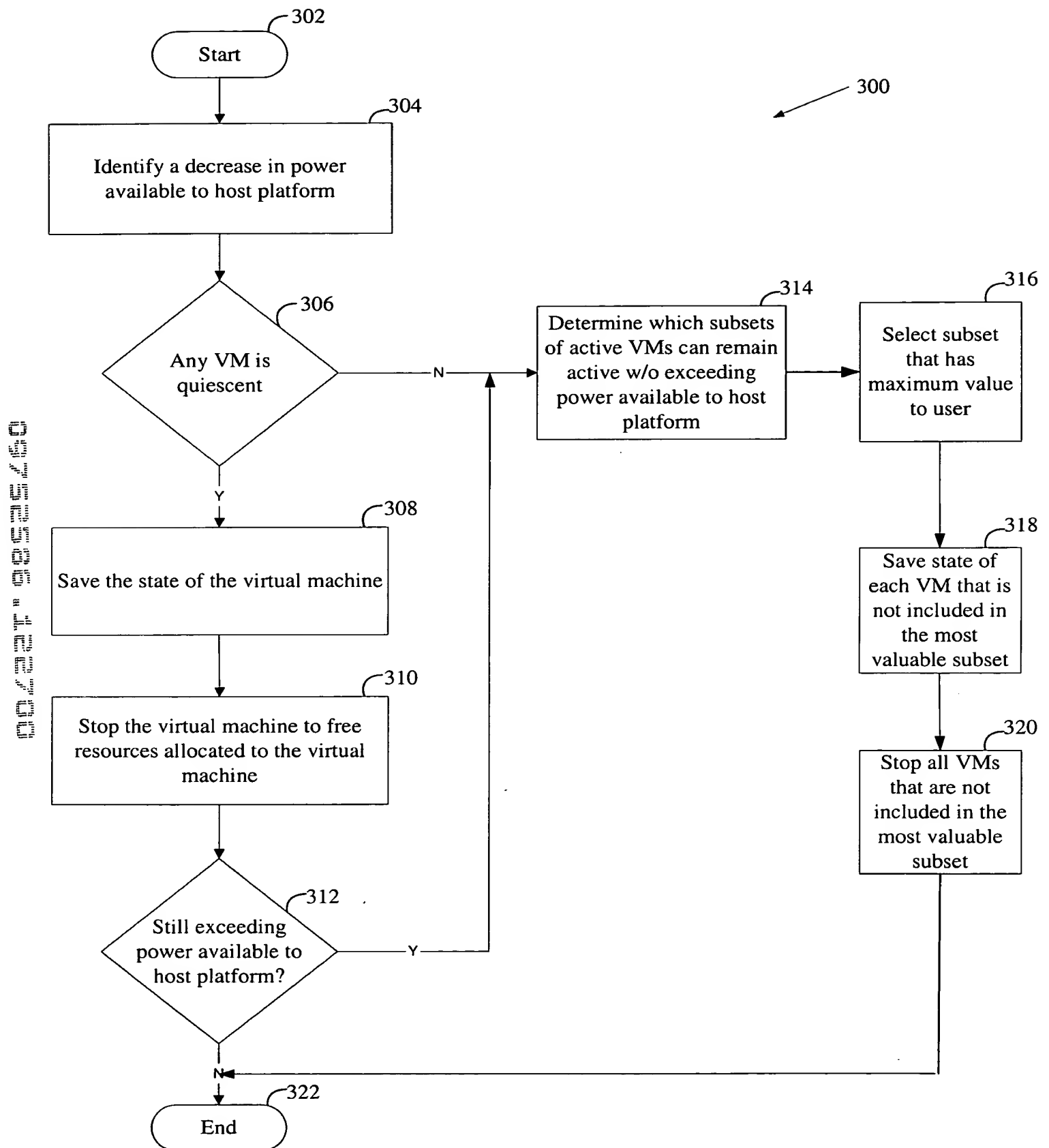


FIG. 1



**FIG. 2**



**FIG. 3**

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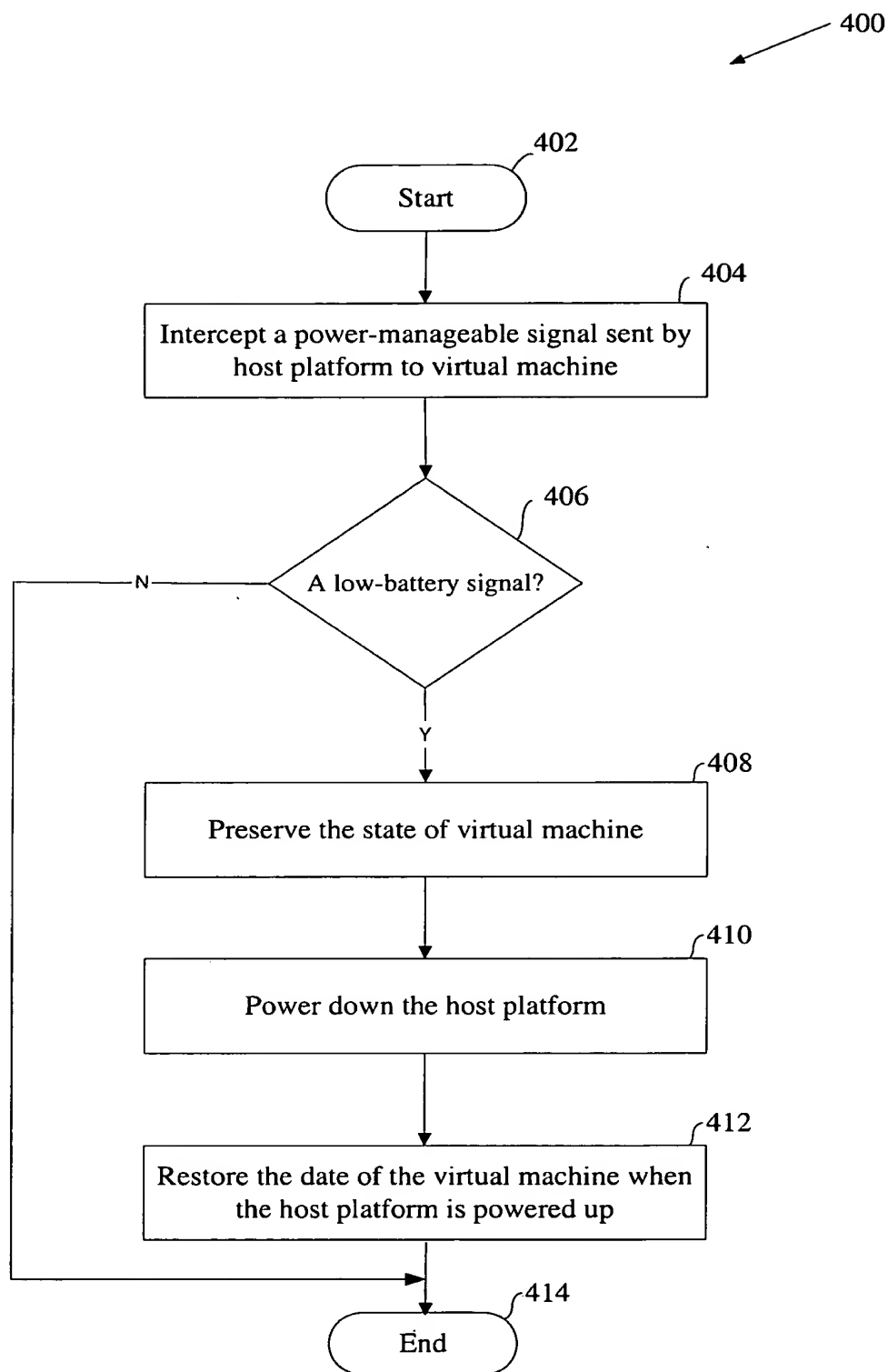


FIG. 4

500

520

PROCESSOR

530

MEMORY

The diagram shows a system 500 consisting of a PROCESSOR (520) and MEMORY (530). The PROCESSOR and MEMORY are represented as rectangular blocks. A bidirectional arrow connects the PROCESSOR and MEMORY, indicating data flow between them.

FIG. 5